MPI RMA Operation Compatibility

	Load	Store	Get	Put	Acc
Load	OVL+NOVL	OVL+NOVL	OVL+NOVL	X	X
Store	OVL+NOVL	OVL+NOVL	X	X	X
Get	OVL+NOVL	X	OVL+NOVL	NOVL	NOVL
Put	X	X	NOVL	NOVL	NOVL
Acc	X	X	NOVL	NOVL	OVL+NOVL

This matrix shows the compatibility of MPI-RMA operations when two or more processes access a window at the same target during overlapping (e.g. shared lock) epochs.

OVL – Overlapping operations permitted
NOVL – Nonoverlapping operations permitted
X – Operations cannot be combined